

PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q79257

Katsuki KUSUNOKI

Appln. No.: 10/581,335

Group Art Unit: 2812

Confirmation No.: 6238

Examiner: Not Yet Assigned

Filed: December 14, 2006

For: METHOD FOR PRODUCTION OF SEMICONDUCTOR CHIP, AND
SEMICONDUCTOR CHIP

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith, along with a copy of the corresponding Communication from a Foreign Patent Office (Chinese Office Action for corresponding Chinese Patent Application No. 200480036072.6 dated August 31, 2007), except for the following: U.S. patents and/or U.S. patent publications; and co-pending non-provisional U.S. applications filed after June 30, 2003.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

JP 10-125958 was previously submitted in the Information Disclosure Statement filed June 2, 2006.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,



Abraham J. Rosner
Registration No. 33,276

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: October 5, 2007

PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q79257

Katsuki KUSUNOKI

Appln. No.: 10/581,335

Group Art Unit: 2812

Confirmation No.: 6238

Examiner: Not Yet Assigned

Filed: December 14, 2006

For: METHOD FOR PRODUCTION OF SEMICONDUCTOR CHIP, AND
SEMICONDUCTOR CHIP

STATEMENT UNDER 37 C.F.R. § 1.97(e)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The undersigned hereby states, upon information and belief:

That each item of information contained in the Information Disclosure Statement filed concurrently herewith was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of said Information Disclosure Statement.

Respectfully submitted,



Abraham J. Rosner
Registration No. 33,276

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: October 5, 2007